Welcome to the World of Standards



World Class Standards

NFV TUTORIAL SESSION - ACCELERATION

NFV#12

Monday 26th October, 12:30 – 14:00

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EPD allow execution of NFVI provided plugins in the context of VNFCIs to allow lightest cost of acceleration abstraction

EPD: Intra-VNFC software execution



Avoid crossing hypervisor boundary Depends on administrative policy Rely on proper trust model

ETS

EPD: portable native hardware access



ETS

EPD: Standard short life cycle with ETSI+OPNFV

- Requirements and functional aspects standardized in ETSI NFV
- Oetailed specifications and Open Source implementation in OPNFV DPACC
- First phase of implementation to be demoed before year end by OPNFV DPACC members
- Commercial products to follow
- Standards + open Source offer the fastest way to interoperability by clearing most/all ambiguities through community led reference implementation (as opposed to test different implementations coded separately)

Acceleration Interfaces

e High performance networking

- Cryptography, IPSec offloading, TCP offloading, Packet dispatching
- e High performance computing
 - GPU computing, RDMA
- e High performance Storage
 - NVMe, ISCSI, other persistent memory schemes
- Oynamic Optimization of Packet Flow Routing
 - Optimize intra-VNF packet forwarding and routing



EI